

REMARKS

The Examiner has required restriction of this application to one of the following inventions:

- I. A printed circuit wiring board set forth in claims 7-12; or
- II. A method of forming a multi-layer printed circuit board set forth in claims 1-6.

Applicant hereby selects Invention II, the method of forming a multi-layer printed circuit board set forth in claims 1-6. Applicant reserves the right to submit any Divisional patent applications directed toward the non-selected invention.

An early action on the merits of this application is respectfully requested. Should any points remain in issue, which the Examiner feels could best be resolved by either a personal or a telephone interview, it is urged that Applicant's local attorney be contacted at the exchange listed below.

Respectfully submitted,

Date: November 10, 2005

By:


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